

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Isamu Kobori et al.                      Art Unit : Unknown  
Serial No. : New Divisional Application              Examiner : Unknown  
Filed : July 22, 2003  
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF  
MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM  
TRANSISTOR

**Attention: Official Draftsman**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

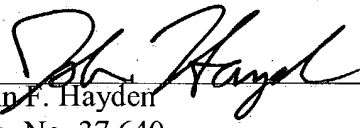
**TRANSMITTAL OF FORMAL DRAWINGS**

Please substitute the enclosed six (6) sheets of formal drawings (Figs. 1-5D) for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 22, 2003

  
\_\_\_\_\_  
John F. Hayden  
Reg. No. 37,640

Fish & Richardson P.C.  
1425 K Street, N.W.  
11th Floor  
Washington, DC 20005-3500  
Telephone: (202) 783-5070  
Facsimile: (202) 783-2331